## RESPONSE UNDER 37 CFR 1.116 **EXPEDITED PROCEDURE EXAMINING GROUP 2816**

PATENT APPLICATION Docket No. 9898-204 Client No. SS-16-038-US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon SIM and Jei-Hwan YOO

Serial No.

09/901,930

Examiner:

Wells, Kenneth

Filed:

July 9, 2001

Art Unit:

2816

Confirmation No.

7100

For:

NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR

MEMORY DEVICE

Date:

September 13, 2007

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT AFTER APPEAL BRIEF UNDER 37 CFR 1.116**

Claim Amendments begin on page 2 of this paper.

Remarks begin on page 4 of this paper.